

Fig. 4

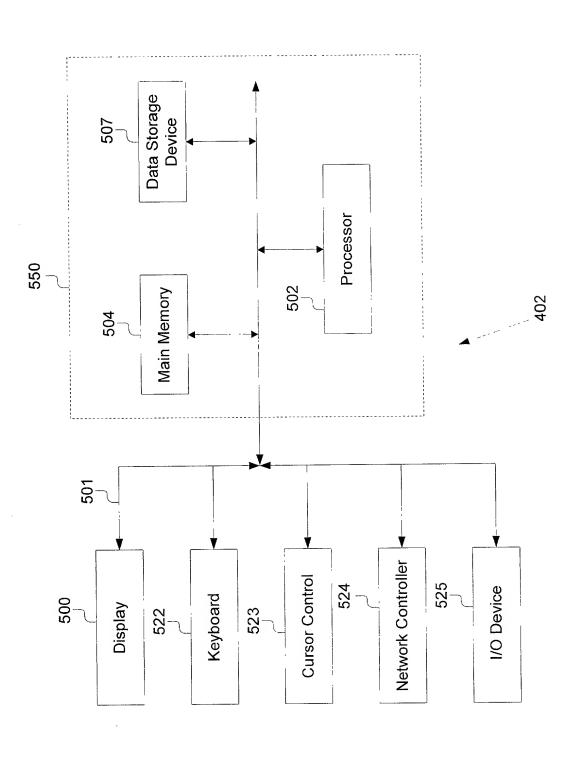


Figure 5

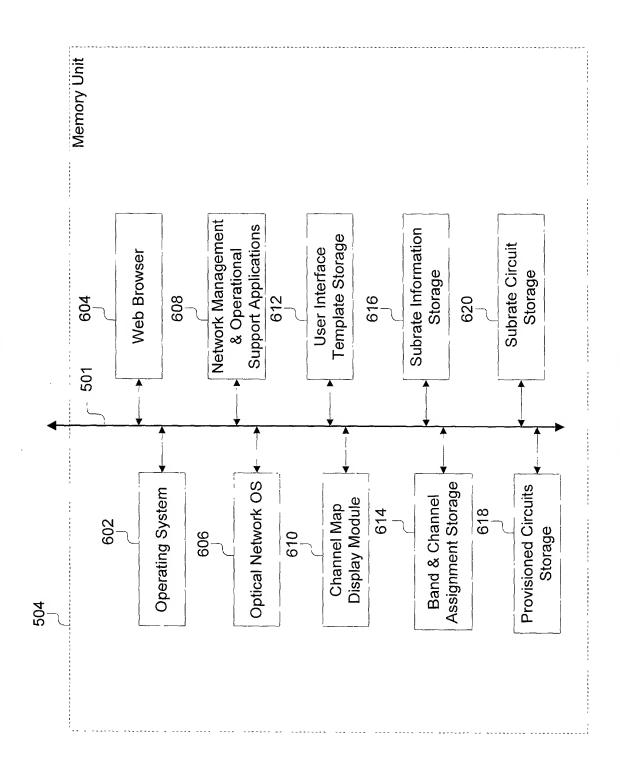


Figure 6

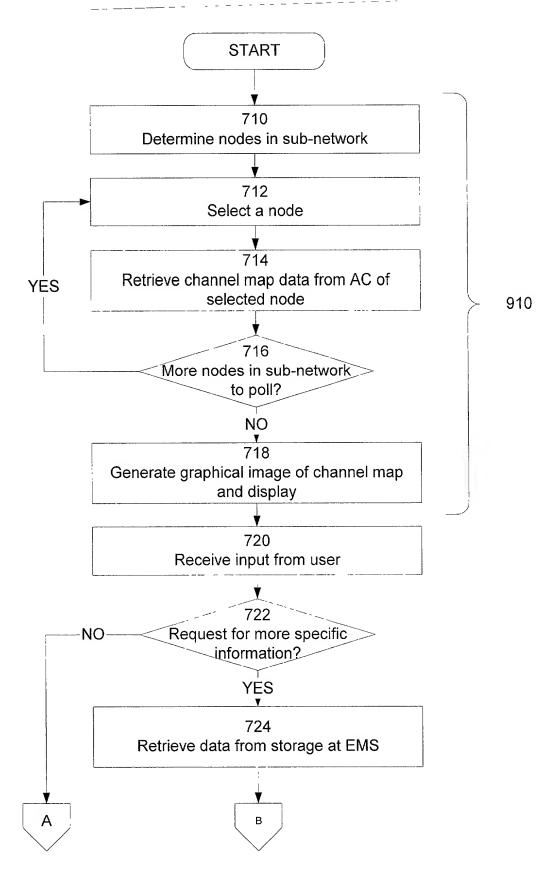


FIG. 7A

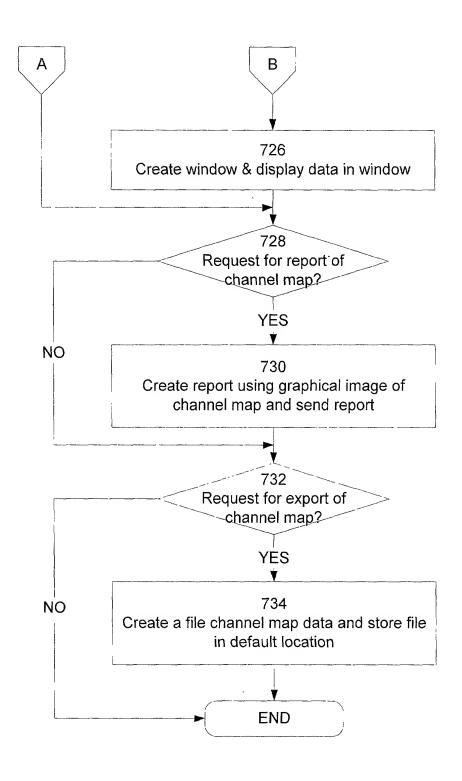


FIG. 7B

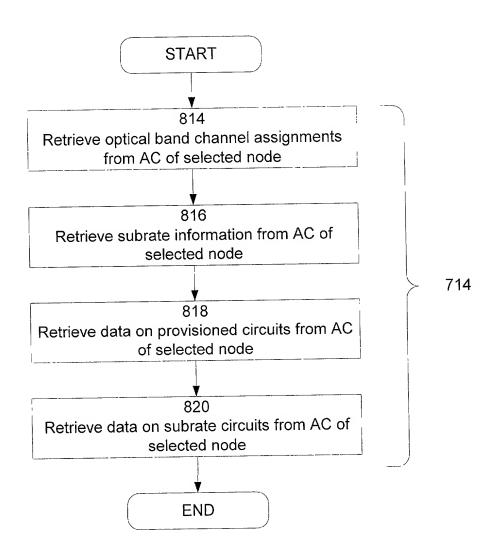


FIG. 8

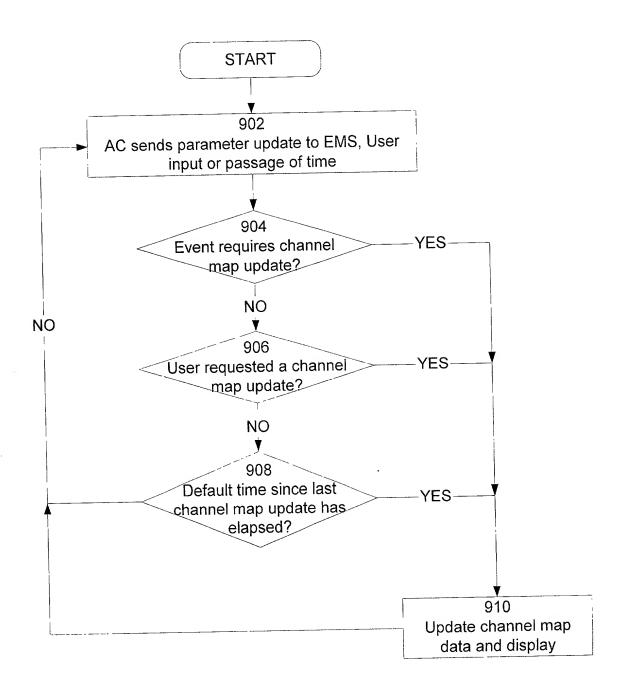


FIG. 9

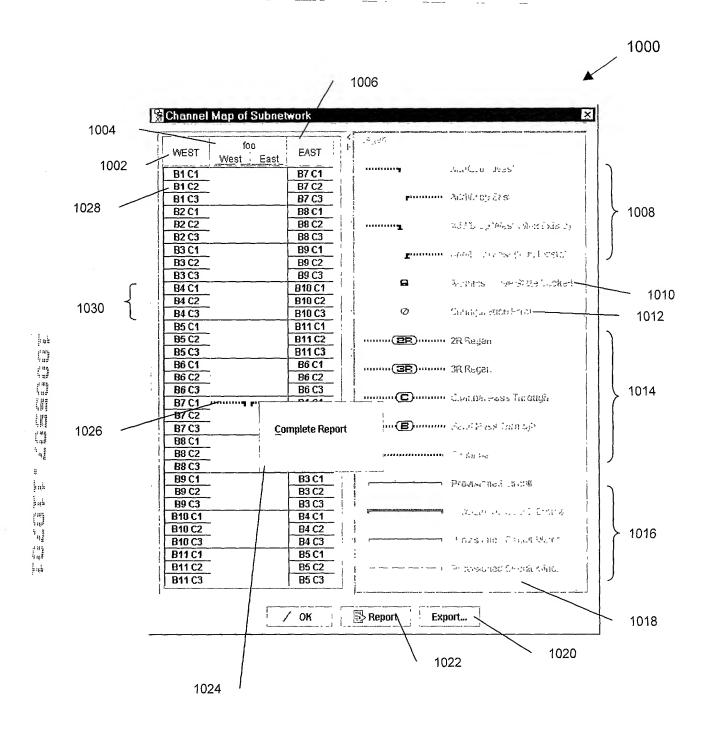


Fig. 10

ļu.

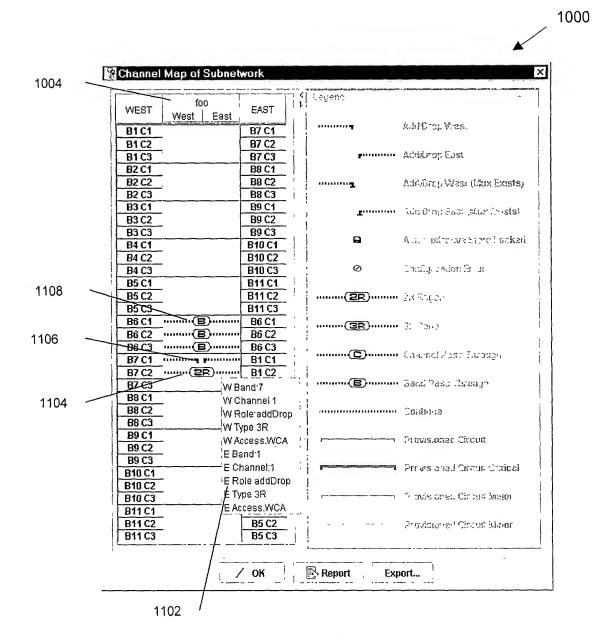


FIG. 11

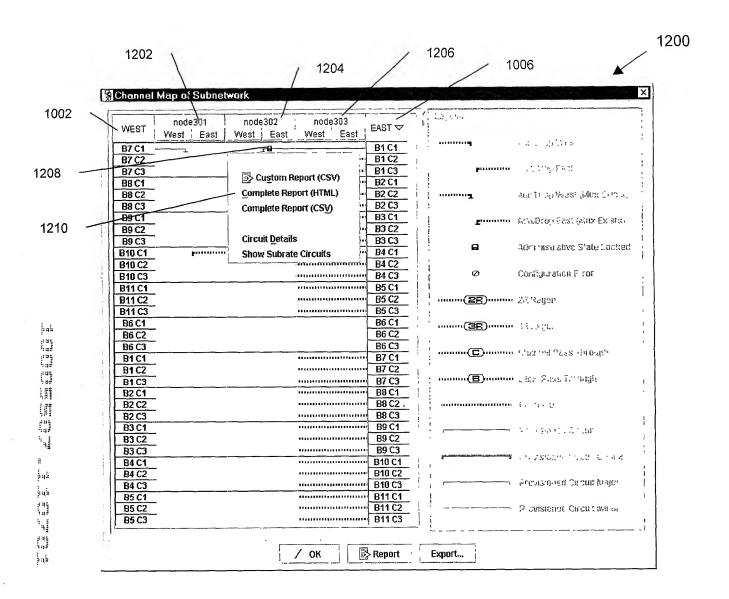


Fig. 12

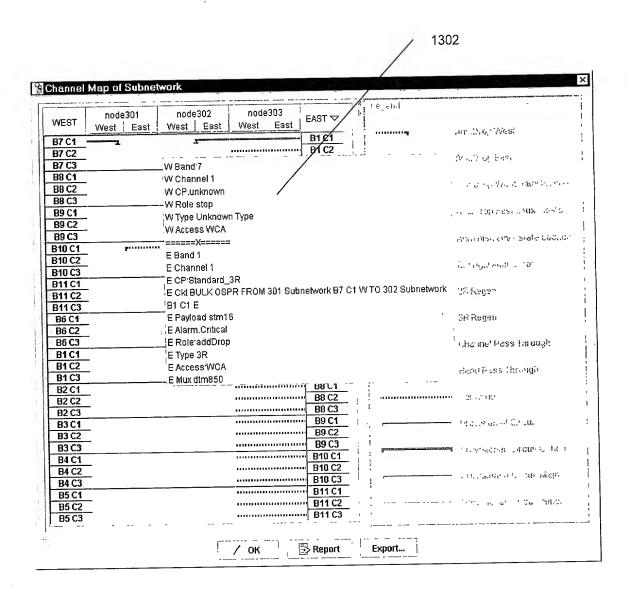


Fig. 13

Circuit List Vie	W								S						X
Circuit T Circuit N.							A Band						Z Band Z		Line Co . Line Co
Bulk Rate BULK 0	Protected OC48/S	locked	enabled	node301	west	7-1-W		7 1	res .	node302	east	1-1-E	. 1	1 Yes	0 N/A
	Protected														
															,
															j
	****** AN ** ** ** A ALLEY ** ************ AT		·					for som to Affiguration							
						/ OK		Report	Ехрог	t					

Fig. 14

Circuit Details dialog

Circuit	List View	r										11 - No.					
Circui 🔽	7 Circuit Na	me Access.	Servic	Admın S	Oper St.	A Node	A Side	ATTP	A Band	A Chan	A Port Exists	Z Node	ZSide	ZTTP	Z BandiZ Ci	na Z Por.	Line Line
Sub Rate	GbE Ckt1	Protected	GbE	locked	enabled	node301	west	GRDM-TR-3-3-	7		1 Yes	node302	east	GRDM-TR-2-3-P1	1	1 Yes	D No
Sub Rate	GbE Ckt2	Protected	GbE	locked	enabled	node301	west	GRDM-TR-3-3-	7		1 Yes	node302	east	GRDM-TR-2-3-P2	1	1 Yes	0 No
					*			′ ок 📜	Report	j	xport						- 42 p

Fig. 15

Subrate Circuits details dialog

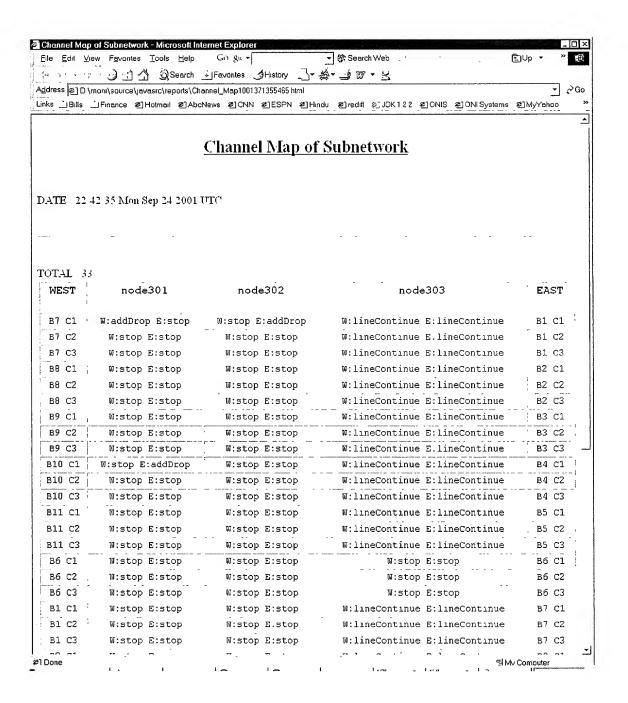


Fig. 16